

EAST History			6/14/2005	10:38 AM
BRS	1041	serial adj peripheral adj interface	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	4	S58 and (serial adj PROM)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	0	S58 and (serial adj ROM)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	26	S58 and (serial adj memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	0	S61 and (programmable adj device)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	1	S61 and (programmable adj logic adj device)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	0	S61 and (field adj programmable adj gate adj array)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	0	S61 and (programmable adj gate adj array)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	0	S58 and (serial adj ROM)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	26	S58 and (serial adj memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
IS&R	1	("6044025").PN.	USPAT; USOCR	
BRS	179	serial adj flash	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	4263	(data adj flash) or dataflash	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	4412	S69 or S70	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	30	S69 and S70	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	125	S72 and ((field adj programmable adj gate adj array) or fpga)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	6866	(serial adj peripheral adj interface) or SPI	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	

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BRS	4	S73 and S74	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	82	S72 and S74	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	4	S76 and ((field adj programmable adj gate adj array) or fpga)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	4	S76 and ((field adj programmable adj gate adj array) or FPGA)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	4	S76 and ((programmable adj gate adj array) or FPGA)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	